

WEST Search History

DATE: Monday, August 23, 2004

| Hide? | Set Name | Query | Hit Count |
|--------------------------|--------------------------------------|---|--------------|
| | <i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i> | | |
| <input type="checkbox"/> | L41 | l39 not L40 | 50 |
| <input type="checkbox"/> | L40 | decod\$3 and L39 | 7 |
| <input type="checkbox"/> | L39 | (5671151 or 5513132 or 5121003).uref. | 57 |
| <input type="checkbox"/> | L38 | cascaded domino logic | 11 |
| <input type="checkbox"/> | L37 | l13 and self tim\$3 | 4 |
| <input type="checkbox"/> | L36 | asynchronous\$2 and l35 | 4 |
| <input type="checkbox"/> | L35 | (5671151 or 5600374 or 5625355 or 5513132 or 5351134 or 5801650 or 5231485 or 5343413 or 4499553).pn. | 9 |
| <input type="checkbox"/> | L34 | l13 and asynchronous\$2 | 54 |
| <input type="checkbox"/> | L33 | L14 and L32 | 0 |
| <input type="checkbox"/> | L32 | (712/300).ccls. | 295 |
| <input type="checkbox"/> | L31 | self timing and L13 | 1 |
| <input type="checkbox"/> | L30 | L13 and L29 | 1 |
| <input type="checkbox"/> | L29 | self timing and asynchronous | 216 |
| <input type="checkbox"/> | L28 | zostil | 3 |
| <input type="checkbox"/> | L27 | variable length with L26 | 4 |
| <input type="checkbox"/> | L26 | decod\$3 with (asynchronous\$2) | 1639 |
| <input type="checkbox"/> | L25 | L23 not L24 | 15 |
| <input type="checkbox"/> | L24 | huffman with (asynchronous\$2) | 29 |
| <input type="checkbox"/> | L23 | huffman same (asynchronous\$2) | 44 |
| <input type="checkbox"/> | L22 | huffman same (self tim\$3) | 0 |
| <input type="checkbox"/> | L21 | huffman with (self tim\$3) | 0 |
| <input type="checkbox"/> | L20 | huffman and L19 | 0 |
| <input type="checkbox"/> | L19 | 5671151.pn. or 5600374.pn. | 2 |
| <input type="checkbox"/> | L18 | shikakura.in. and decoder | 11 |
| <input type="checkbox"/> | L17 | ring and L16 | 12 |
| <input type="checkbox"/> | L16 | williams.in. and input buffer | 76 |
| <input type="checkbox"/> | L15 | 5671151.pn. and 5600374.pn. | 0 |
| <input type="checkbox"/> | L14 | L13 and L4 | 2 |
| <input type="checkbox"/> | L13 | (341/65).ccls. or (341/67).ccls. | 774 |

| | | | |
|--------------------------|-----|-------------------------------|-----|
| <input type="checkbox"/> | L12 | L4 same variable length | 2 |
| <input type="checkbox"/> | L11 | instruction same L5 | 1 |
| <input type="checkbox"/> | L10 | variable length same L5 | 0 |
| <input type="checkbox"/> | L9 | huffman same L5 | 0 |
| <input type="checkbox"/> | L8 | huffman with L5 | 0 |
| <input type="checkbox"/> | L7 | variable length with L5 | 0 |
| <input type="checkbox"/> | L6 | variabl length with L5 | 0 |
| <input type="checkbox"/> | L5 | decoder with ring with logic | 78 |
| <input type="checkbox"/> | L4 | decod\$3 with ring with logic | 159 |
| <input type="checkbox"/> | L3 | decod\$3 and L2 | 26 |
| <input type="checkbox"/> | L2 | ring logic | 72 |
| <input type="checkbox"/> | L1 | counterflow pipeline | 39 |

END OF SEARCH HISTORY